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## Search Results - Record(s) 1 through 10 of 12 returned.

### ☐ 1. Document ID: NN9511419

L5: Entry 1 of 12                    File: TDBD                    Nov 1, 1995

TDB-ACC-NO: NN9511419

DISCLOSURE TITLE: Tracing Hardware for a Multiplexed Interface

PUBLICATION-DATA:
IBM Technical Disclosure Bulletin, November 1995, US

VOLUME NUMBER: 38
ISSUE NUMBER: 11
PAGE NUMBER: 419 - 420

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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### ☐ 2. Document ID: NN941293

L5: Entry 2 of 12                    File: TDBD                    Dec 1, 1994

TDB-ACC-NO: NN941293

DISCLOSURE TITLE: Direct Memory Access Controller for DASD Array Controller

PUBLICATION-DATA:
IBM Technical Disclosure Bulletin, December 1994, US

VOLUME NUMBER: 37
ISSUE NUMBER: 12
PAGE NUMBER: 93 - 98

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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⅃ 3.   Document ID: NN9207297

L5: Entry 3 of 12                         File: TDBD                    Jul 1, 1992

TDB-ACC-NO: NN9207297

DISCLOSURE TITLE: Switch Chip for a Multiprocessing System.

PUBLICATION-DATA:
IBM Technical Disclosure Bulletin, July 1992, US

VOLUME NUMBER: 35
ISSUE NUMBER: 2
PAGE NUMBER: 297 - 300

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☐ 4.   Document ID: NN8906396

L5: Entry 4 of 12                         File: TDBD                    Jun 1, 1989

TDB-ACC-NO: NN8906396

DISCLOSURE TITLE: Simultaneous Dual Port Direct Memory Access Transfers

PUBLICATION-DATA:
IBM Technical Disclosure Bulletin, June 1989, US

VOLUME NUMBER: 32
ISSUE NUMBER: 1
PAGE NUMBER: 396 - 404

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⅃ 5.   Document ID: NN8812470

L5: Entry 5 of 12                         File: TDBD                    Dec 1, 1988

TDB-ACC-NO: NN8812470

DISCLOSURE TITLE: Automotive Engine Analyzer High Speed Data Card

PUBLICATION-DATA:
IBM Technical Disclosure Bulletin, December 1988, US

VOLUME NUMBER: 31
ISSUE NUMBER: 7
PAGE NUMBER: 470 - 473

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments |   KWIC   |
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☐ 6.  Document ID: NN8808303

L5: Entry 6 of 12                      File: TDBD                      Aug 1, 1988

TDB-ACC-NO: NN8808303

DISCLOSURE TITLE: High Performance Microprocessor Memory System

PUBLICATION-DATA:
IBM Technical Disclosure Bulletin, August 1988, US

VOLUME NUMBER: 31
ISSUE NUMBER: 3
PAGE NUMBER: 303 - 306

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments |   KWIC   |
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☐ 7.  Document ID: NN8706172

L5: Entry 7 of 12                      File: TDBD                      Jun 1, 1987

TDB-ACC-NO: NN8706172

DISCLOSURE TITLE: Increasing Memory Reliability Through Address Translation and Per
Page Bit Swapping

PUBLICATION-DATA:
IBM Technical Disclosure Bulletin, June 1987, US

VOLUME NUMBER: 30
ISSUE NUMBER: 1
PAGE NUMBER: 172 - 175

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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☐ 8.  Document ID: NN83024658

L5: Entry 8 of 12                    File: TDBD                    Feb 1, 1983

TDB-ACC-NO: NN83024658

DISCLOSURE TITLE: Signature Analysis Functional Tester. February 1983.

PUBLICATION-DATA:
IBM Technical Disclosure Bulletin, February 1983, US

VOLUME NUMBER: 25
ISSUE NUMBER: 9
PAGE NUMBER: 4658 - 4661

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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☐ 9.  Document ID: NA82081216

L5: Entry 9 of 12                    File: TDBD                    Aug 1, 1982

TDB-ACC-NO: NA82081216

DISCLOSURE TITLE: Testing of Memory Parts. August 1982.

PUBLICATION-DATA:
IBM Technical Disclosure Bulletin, August 1982, US

VOLUME NUMBER: 25
ISSUE NUMBER: 3A
PAGE NUMBER: 1216 - 1227

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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☐   10.   Document ID: NN78122633

L5: Entry 10 of 12                          File: TDBD                    Dec 1, 1978

TDB-ACC-NO: NN78122633

DISCLOSURE TITLE: Programmable Communications Subsystem Having Controller
Incorporating Microprocessor. December 1978.

PUBLICATION-DATA:
IBM Technical Disclosure Bulletin, December 1978, US

VOLUME NUMBER: 21
ISSUE NUMBER: 7
PAGE NUMBER: 2633 - 2645

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | | KWIC |
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## Search Results - Record(s) 11 through 12 of 12 returned.

☐ 11.  Document ID: NN76043707

L5: Entry 11 of 12                          File: TDBD                     Apr 1, 1976

TDB-ACC-NO: NN76043707

DISCLOSURE TITLE: Memory Prefetch. April 1976.

PUBLICATION-DATA:
IBM Technical Disclosure Bulletin, April 1976, US

VOLUME NUMBER: 18
ISSUE NUMBER: 11
PAGE NUMBER: 3707 - 3708

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments |     | KWIC |
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☐ 12.  Document ID: NN73033230

L5: Entry 12 of 12                          File: TDBD                     Mar 1, 1973

TDB-ACC-NO: NN73033230

DISCLOSURE TITLE: Digital Interface Recorder. March 1973.

PUBLICATION-DATA:
IBM Technical Disclosure Bulletin, March 1973, US

VOLUME NUMBER: 15
ISSUE NUMBER: 10
PAGE NUMBER: 3230 - 3231

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments |     | KWIC |
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